



FIG.1

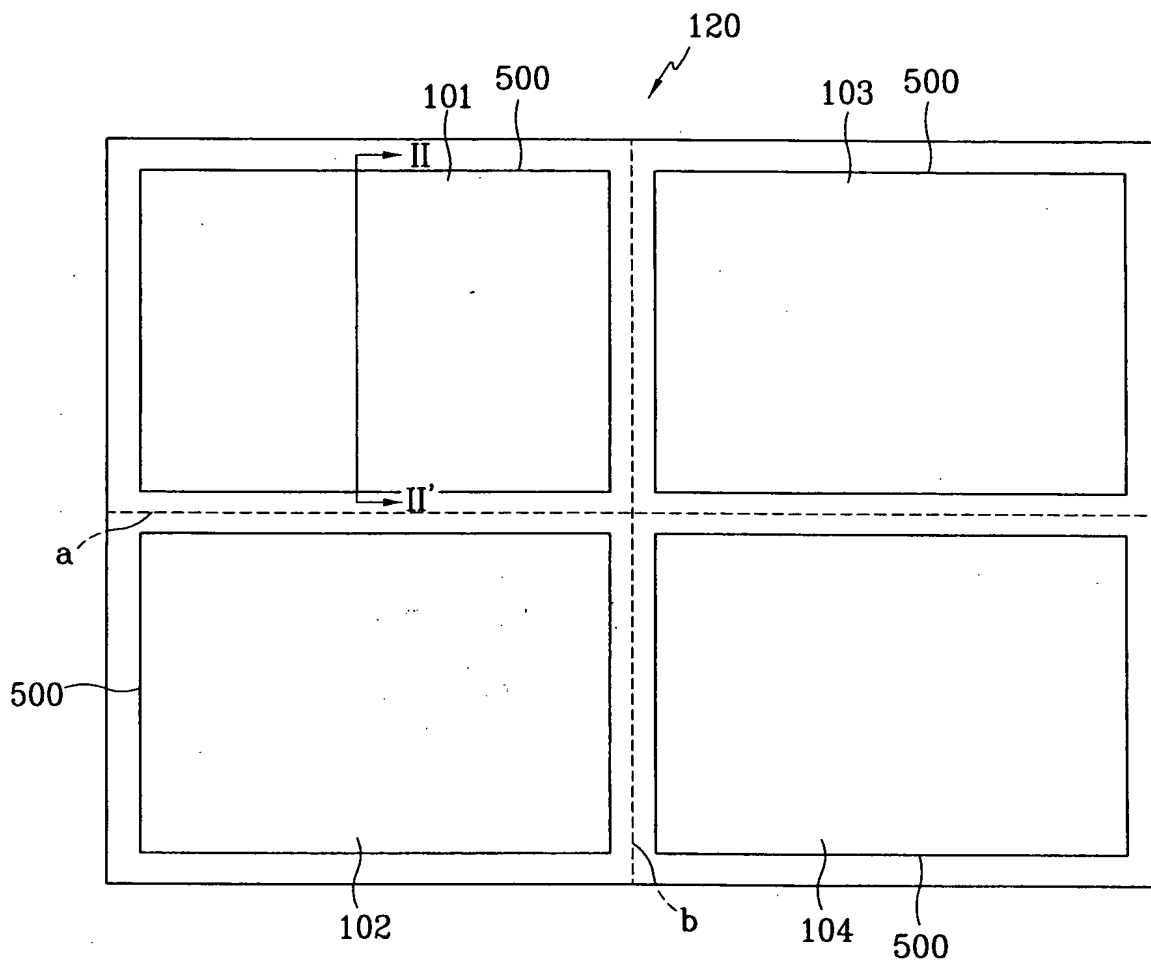


FIG.2

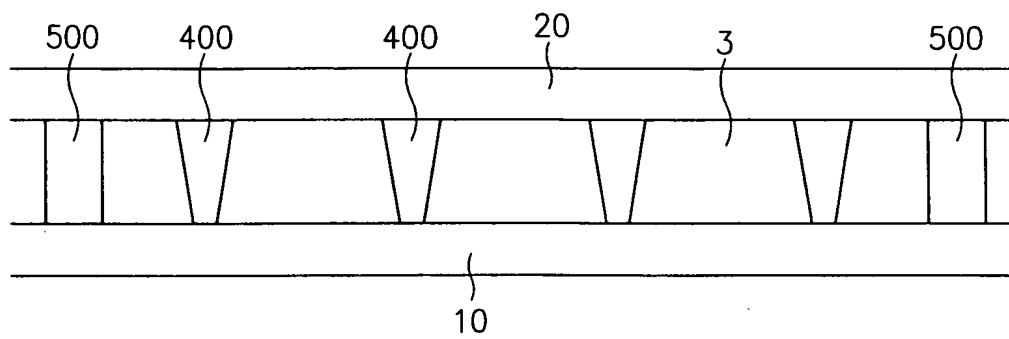


FIG.3

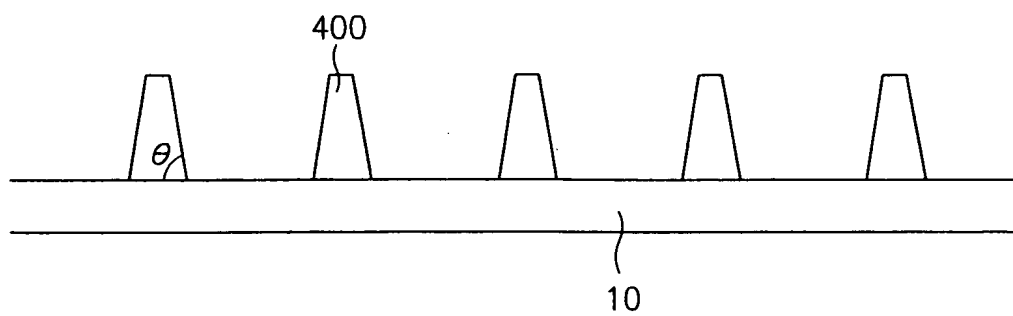
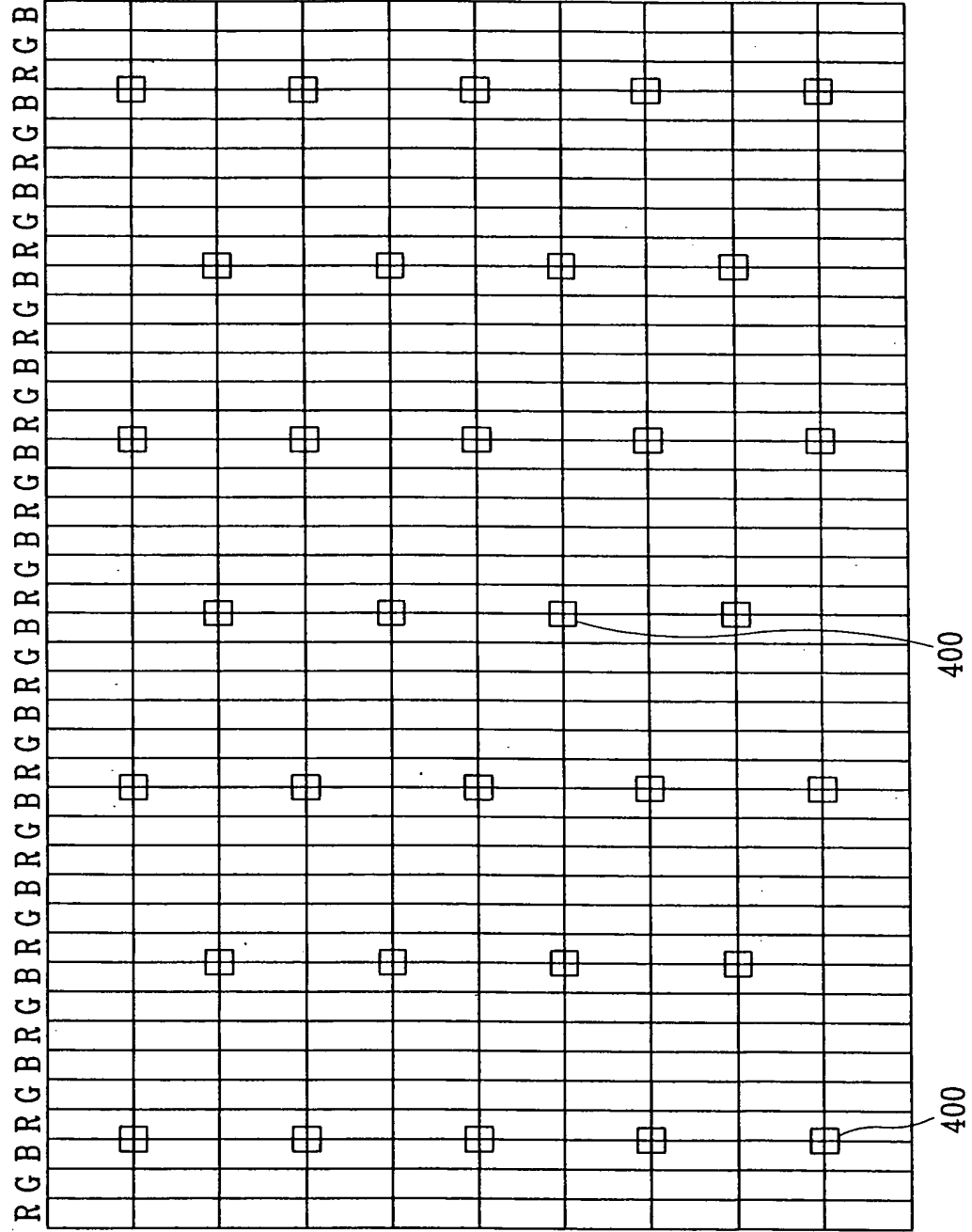


FIG. 4



This schematic diagram illustrates a semiconductor device layout. A central rectangular region, labeled 131, is surrounded by a complex network of wiring and electrical components. The wiring includes a top horizontal line 171, a right vertical line 190, and a bottom horizontal line 220. Various electrical components are distributed around the perimeter: a component 81 is at the top left, 82 at the top right, 129 at the bottom left, and 150 at the bottom right. Other components include 121, 124, 173, 175, 181, 182, 185, and 179. A central vertical line 131 is also shown. The layout is bounded by lines labeled VI and VI'.

FIG.6

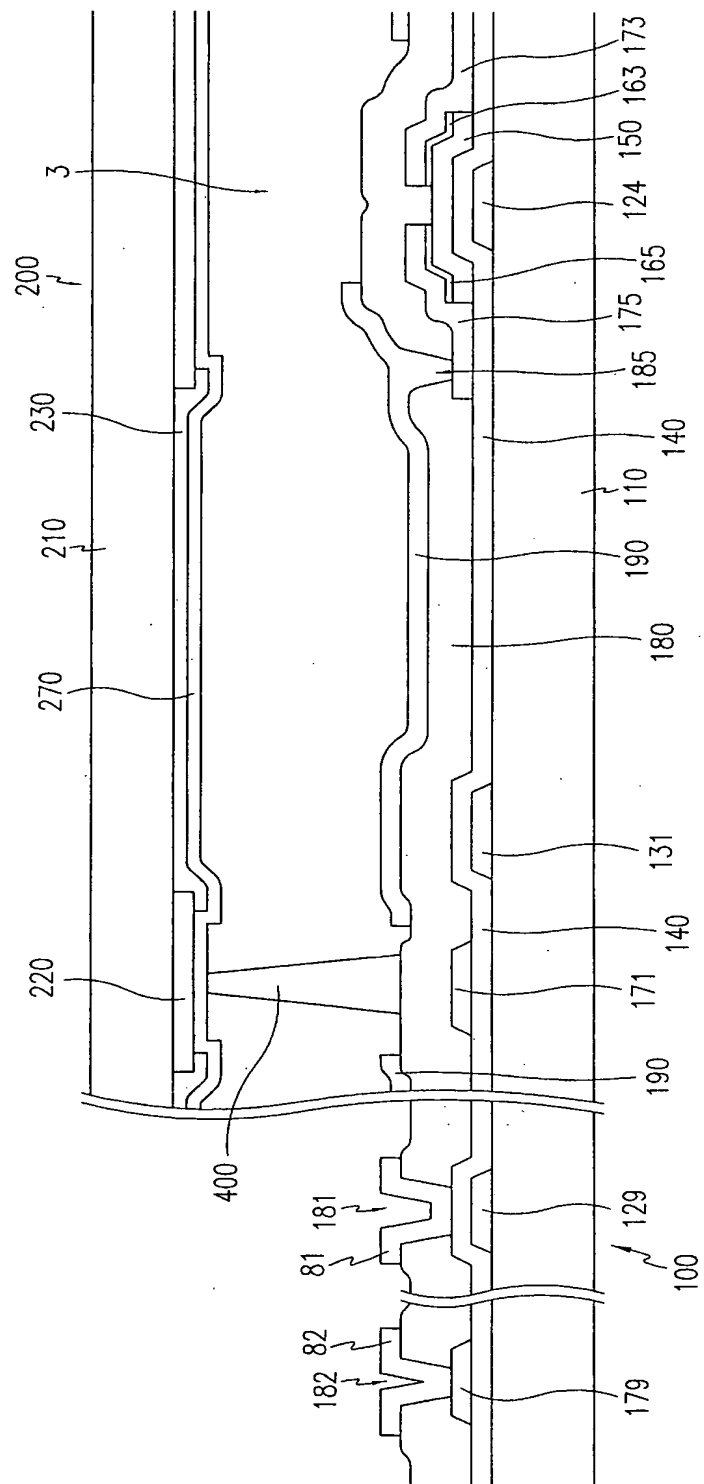


FIG. 7

